

Fig.2

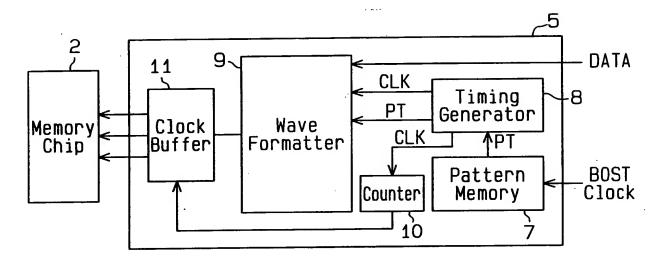
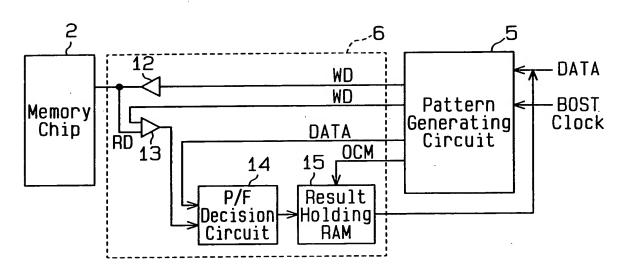
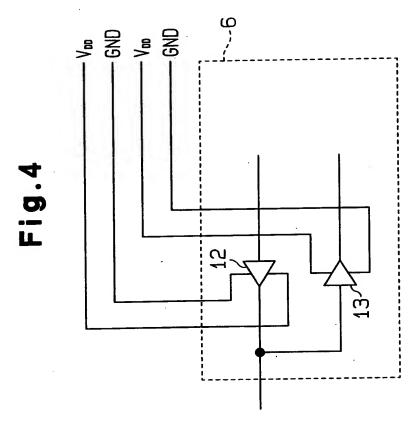


Fig.3





٧, ~ BOST-IN 18 16 \ SS 2g / g V₀₀₁ Memory Chip \ Ss a

Fig.6

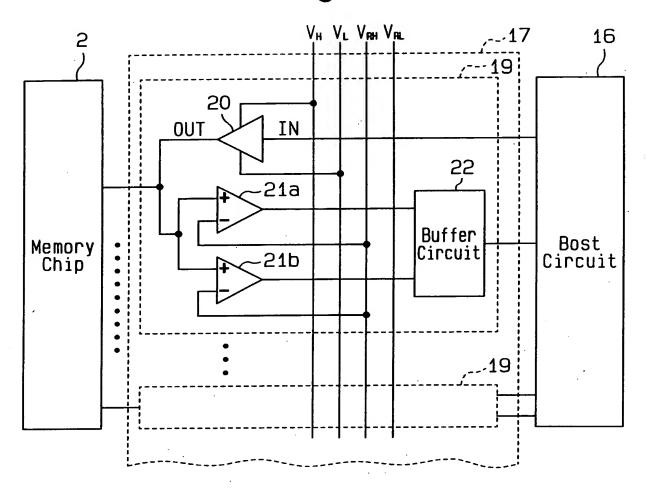


Fig.7

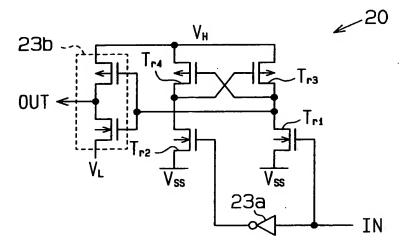


Fig. 8

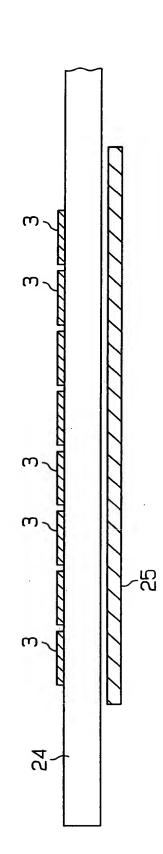


Fig.9

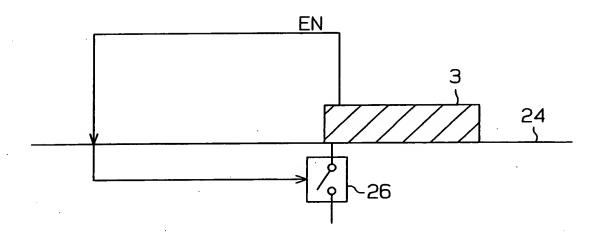
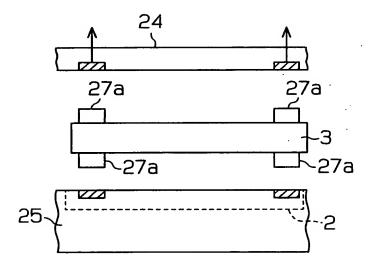


Fig.10



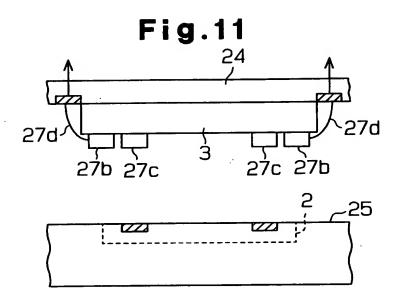
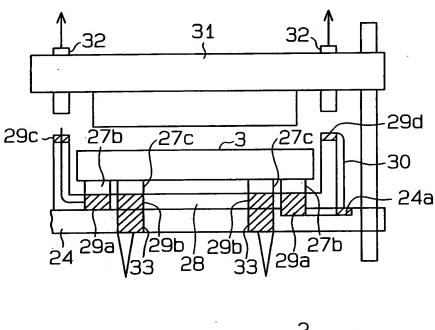


Fig.12



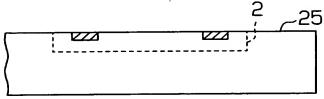
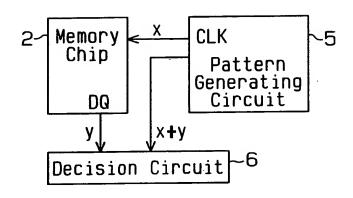


Fig.13



CLK Latch Circuit 35

Selection Frequency 36
Circuit Counter 38

Access Time Determining Circuit

Fig.15

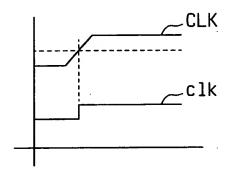


Fig.16

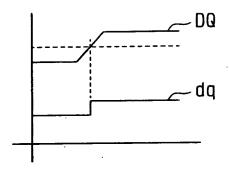


Fig.17

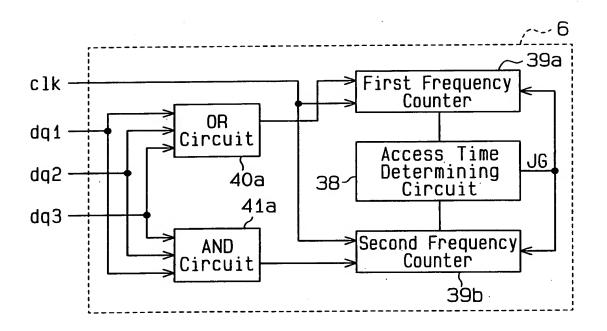


Fig.18

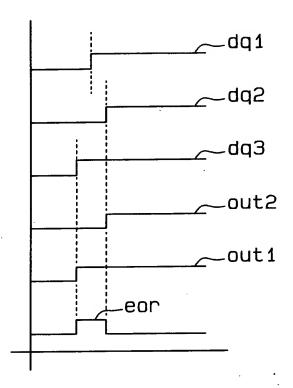


Fig.19

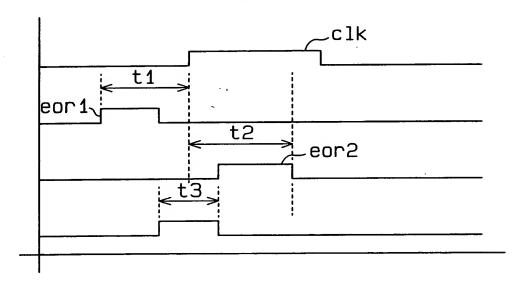


Fig.20

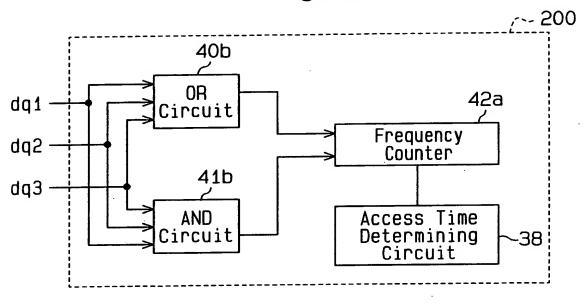


Fig.21

